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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/985,020 11/01/2001 PAT 6,646,306 *AR*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-354043 11/21/2000 *AR*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR  COUNTRY JAPAN	SHEETS  DRAWING 34	TOTAL  CLAIMS 17	INDEPENDENT  CLAIMS 6
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged  Examiner's Signature <i>Andrew...</i> Initials				

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## TITLE

Semiconductor device and method of manufacturing the same

☐ All Fees